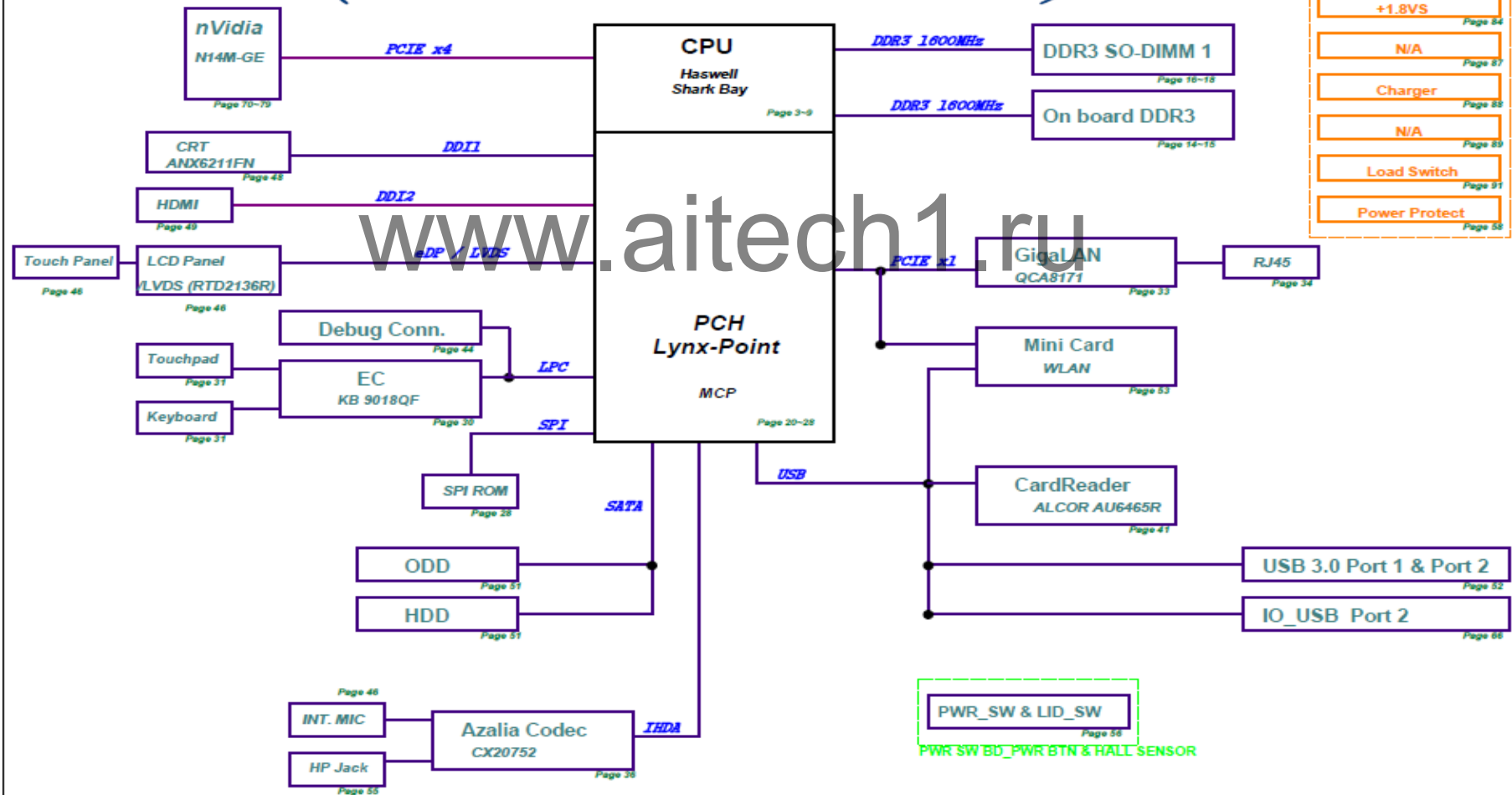


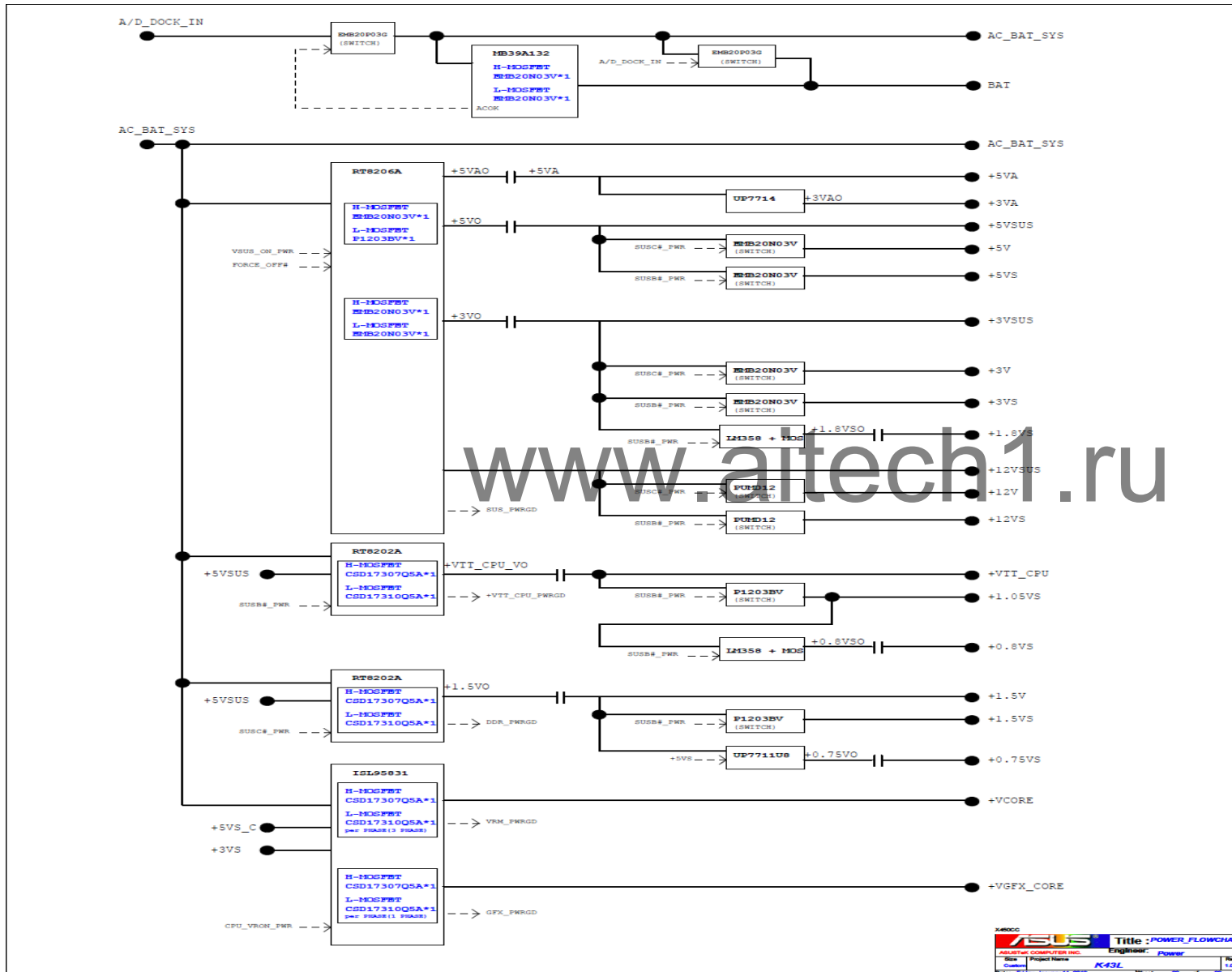
BLOCK DIAGRAM

BLOCK DIAGRAM

X450LC SCHEMATIC R2.0 (DGPU=Nvidia N14M-GE)



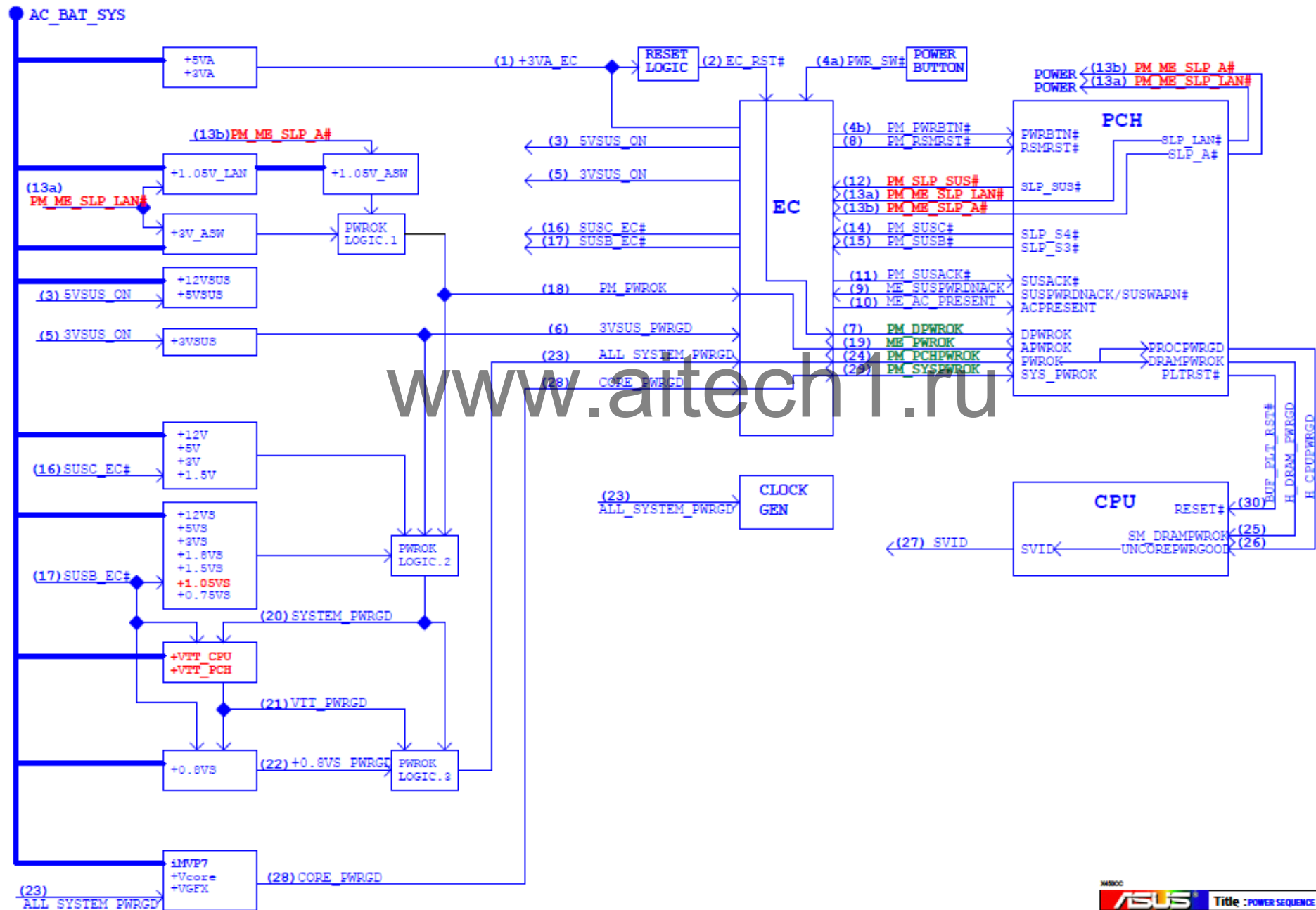
POWER FLOW



POWER ON SEQUENCE

AC-IN Mode

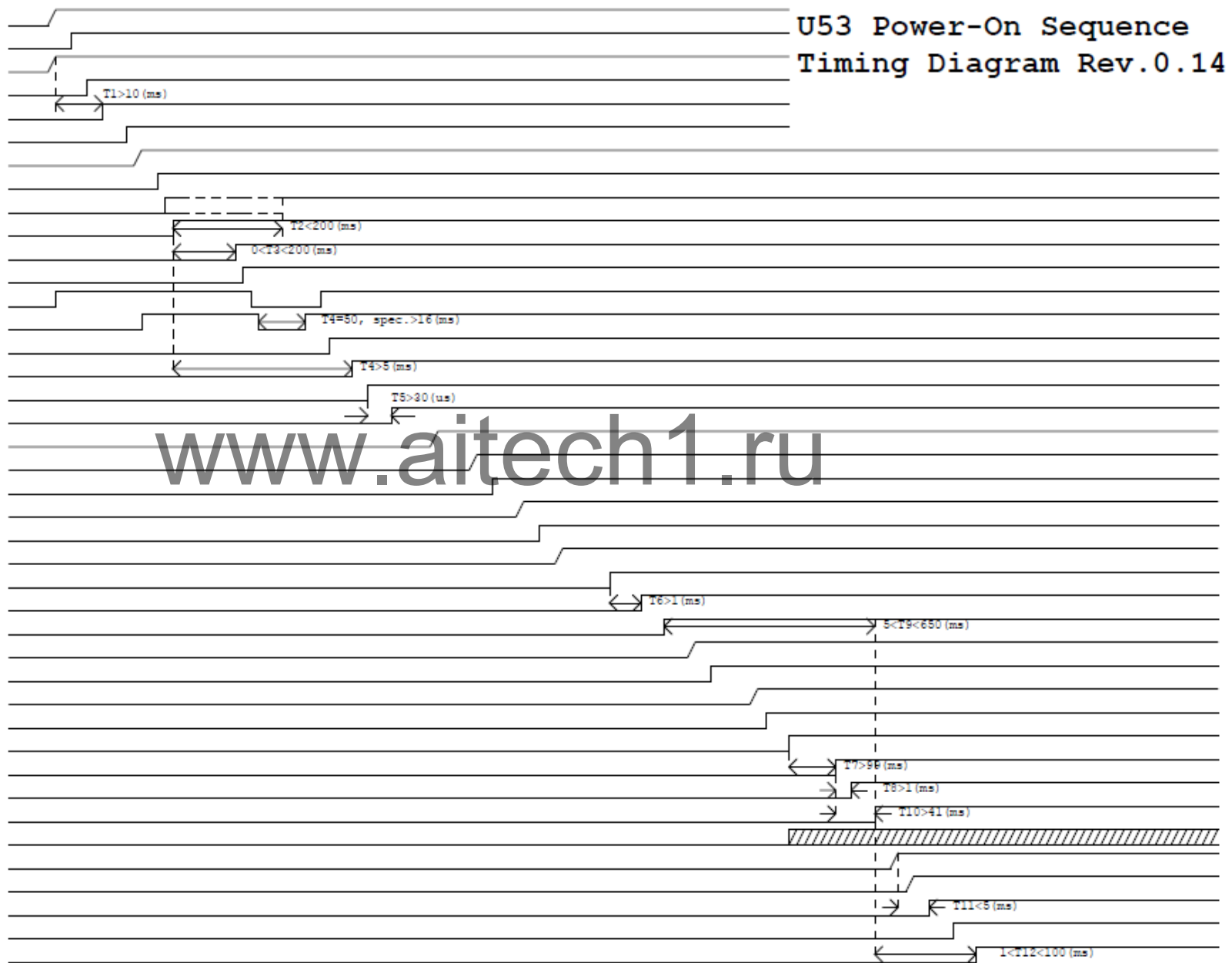
Power On Sequence Diagram Rev.



AC POWER ON SEQUENCE

AC-IN Mode

- 1 +3VA/+5VA/+3VA_EC
- 2 EC_RST#
- 3 PM_SLP_SUS#
- 4 PM_DPWR0K
- 5 VSUS_ON
- +3VSUS/+5VSUS
- SUS_PWRGD
- 7 ME_SusPwrDnAck
- 8 PM_RSMRST#
- 9 ME_AC_PRESENT
- 10 PM_SUSACK#
- 11 FWR_SW#
- 12 PM_PWRBTN#
- 13(a) PM_ME_SLP_LAN#
- 13(b) PM_ME_SLP_A#
- 14 PM_SUSC#
- 15 PM_SUSB#
- +105VM_LAN
- +1.05VM/+3VM
- 16 SUSC_EC#
- +1.5V/+2V/+5V
- 17 SUSB_EC#
- +0.75VS/+1.5VS//+1.6VS/+3VS/+5VS
- 18 ME_+VM_PWRGD
- 19 ME_PWROK
- 20 SYSTEM_PWRGD
- +VTT_CPU
- 21 +VTT_CPU_PWRGD
- +0.8VS
- 22 +0.8VS_PWRGD
- 23 ALL_SYSTEM_PWRGD
- 24 PM_PCHPWROK
- 25 H_DRAM_PWRGD
- 26 H_CPUPWRGD
- 27 SVID
- +VCORE
- +VccGFX
- 28 VRM_PWRGD
- 29 PM_SYSPWROK
- 30 BUF_PLT_RST#



DC POWER ON SEQUENCE

DC-IN Mode

U53 Power-On Sequence
Timing Diagram Rev.0.14



Signal Measure Point-Bottom



Signal Measure Point-Top

